

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the Application of:

Alexander MacInnis, et al.

U.S. Serial No.: 10/763,087

Filed: January 22, 2004

For: GRAPHICS DISPLAY SYSTEM  
WITH UNIFIED MEMORY  
ARCHITECTURE

Examiner: Hassan, Aurangzeb

Group Art Unit: 2182

Confirmation No.: 6408

**ELECTRONICALLY FILED**  
**On August 13, 2007**

**LITIGATION STATEMENT PURSUANT TO MPEP § 2001.06(C) and INFORMATION**  
**DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The owner of record of the present application, Broadcom Corporation, is currently involved in a patent infringement action with Qualcomm, Inc., Case No. 06-CV-0660 B, pending in the Southern District of California.

Pursuant to MPEP 2001.06(c), the Applicants would like to bring to the attention of the U.S. Patent and Trademark Office that the subject matter currently being claimed in the present application may be related to at least one of the patents currently asserted by Broadcom in the above-mentioned lawsuit.

The Applicants have also attached with this electronic submission a completed PTO form PTO/SB/08A which has six (6) pages. Copies of each printed reference listed in the PTO/SB/08A form are attached with this electronic submission. Applicants, however, have not submitted U.S.

Patents or other references previously provided to or by the PTO in this application. Fifty-seven (57) references are attached in this electronic submission.

The references being submitted have been either cited, produced or relied upon by Qualcomm thus far during the above-mentioned lawsuit. This submission is in no way intended as an admission that the submitted references constitute prior art under any subsection of 35 U.S.C. §102 or §103. Applicant expressly retains the right to argue that any of the cited references are not indeed prior art or to take any actions necessary to remove any of the cited references from the available prior art.

The Examiner is requested to initial the attached PTO/SB/08A and return one copy to the applicants to indicate consideration of the attached references.

A fee in the amount of \$180 is believed to be due because:

- \* The applicant(s) believe(s) that this statement and enclosures are being filed after the first Office action on the merits has been mailed by the PTO. The basis of this belief is that an Office action on the merits appears to have been received by the undersigned to date.

The Commissioner is hereby authorized to charge any fees which are presently required, or credit any overpayment, to Deposit Account No. 13-0017.

Respectfully submitted,

Date: August 13, 2007

/phs/  
Philip Henry Sheridan  
Reg. No. 59,918

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 6

**Complete if Known**

Application Number	10/763,087
Filing Date	January 22, 2004
First Named Inventor	A. MacInnis
Group Art Unit	2182
Examiner Name	H. Aurangzeb
Attorney Docket Number	17403US05

**U.S. PATENT DOCUMENTS**

Examiner Initial*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	A1	4,020,332	04-26-1977	Crochiere et al.	
	A2	4,367,466	01-04-1983	Takeda et al.	
	A3	4,532,547	07-30-1985	Bennett	
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**EXAMINER  
SIGNATURE****DATE CONSIDERED**

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard St. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450 Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. Send TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet 2 Of 6

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First Named Inventor	A. MacInnis
Group Art Unit	2182
Examiner Name	H. Aurangzeb
Attorney Docket Number	17403US05

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		Number-Kind Code <sup>2</sup> (if known)			
	A40	5,625,611	04-29-1997	Yokota et al.	
	A41	5,625,764	04-29-1997	Tsujimoto et al.	
	A42	5,635,985	06-03-1997	Boyce et al.	
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Sheet 3 Of 6

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First Named Inventor	A. MacInnis
Group Art Unit	2182
Examiner Name	H. Aurangzeb
Attorney Docket Number	17403US05

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Sheet 4 Of 6

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First Named Inventor	A. MacInnis
Group Art Unit	2182
Examiner Name	H. Aurangzeb
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		Number-Kind Code <sup>2</sup> (if known)			
	A130	6,184,908	02-06-2001	Chan et al.	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
	B1	EP 0746116 A2	12-04-1996	Hiratsuka		
	B2	EP 0 752 695	01-08-1997	O'Sullivan		
	B3	EP 0840276 A2	05-06-1998	Benbassat		
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	B6	GB 2287627 A	03-01-1995	Wong		
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Sheet	5	Of	6		

OTHER ART -- NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C1	Plaintiff and Counterdefendant Qualcomm Incorporated's Preliminary Invalidity Contentions For U.S. Patent 6,501,480 and 6,570,579
	C2	Thomas Porter & Tom Duff, "Compositing Digital Images", Computer Graphics, Vol. 18, No. 3, July 1984, pp. 253-259
	C3	Akeley, K. & Jermoluk T., "High-Performance Polygon Rendering," 22 Computer Graphics No. 4 (ACM August 1988).
	C4	Apgar, B., et al., "A Display System for the Stellar Graphics Superconductor Model GS1000," 22 Computer Graphics No. 4 (ACM August 1988).
	C5	Asal, M.D., et al., "Novel Architecture for a High Performance Full Custom Graphics Processor," (IEEE 1989).
	C6	Awaga, M., et al., "3D Graphics Processor Chip Set," (IEEE December 1995).
	C7	Cox, M. & Bhandari, N., "Architectural Implications of Hardware-Accelerated Bucket Rendering on the PC," (Association for Computing Machinery SIGGRAPH/Eurographics Workshop 1997).
	C8	Cyrix Corporation Manual, copyrighted 1996, 1997, and all underlying development work.
	C9	Deering, M., et al., "The Triangle Processor and Normal Vector Shader: A VLSI System for High Performance Graphics," 22 Computer Graphics No. 4 (ACM August 1988).
	C10	Donovan, W., et al., "Pixel Processing in a Memory Controller," (IEEE Computer Graphics and Applications 1995).
	C11	Dutton, T., "The Design of the DEC 3000 Model 500 AXP Workstation," (IEEE 1993).
	C12	Foley, James, D., et al., "Introduction to Computer Graphics," (Addison-Wesley Publishing Co. 1994) ("Introduction to Computer Graphics").
	C13	Galbi, D., et al., "An MPEG-1 Audio/Video Decoder with Run-Length Compressed Antialiased Video Overlays," (1995 IEEE International Solid-State Circuits Conference).
	C14	Glaskowsky, Peter, "Cyrix Creates Ultimate CPU for Games," Microprocessor Report, December 8, 1997.
	C15	Glaskowsky, Peter, "Intel Displays 740 Graphics Chip: Auburn Sets New Standard for Quality - But Not Speed," Microprocessor Report No. 2, February 16, 1998.
	C16	Gwennap, L., "Verite: A Programmable 3D Chip: Rendition Uses Internal RISC CPU for Versatility, Performance," 10 Microprocessor Report No. 6, May 6, 1996.
	C17	Hosotani S., et al., "A Display Processor Conforming to all DTV Formats with 188-TAP FIR Filters and 284 Kb FIFO Memories," (IEEE 1997 (Manuscript received June 13, 1997)).
	C18	Knittel, G. & Straber, W., "VIZARD - Visualization Accelerator for Realtime Display," (Association for Computing Machinery SIGGRAPH/Eurographics Workshop 1997).
	C19	Lewis, R., et al., "Delivering PCI in HP B-Class and C-Class Workstations: A Case Study in the Challenges of Interfacing with Industry Standards," (Hewlett-Packard Journal May 1998).
	C20	Manepally, R. & Sprague, D., "Intel's i750 (R) Video Processor -- The Programmable Solution," (IEEE 1991).
	C21	McCormack, J., et al., "Neon: A Single-Chip 3D Workstation Graphics Accelerator," (SIGGRAPH Eurograph Workshop Graph Hardware August 31, - Sept. 1, 1998).
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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
		Application Number	10/763,087
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	January 22, 2004
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		Group Art Unit	2182
		Examiner Name	H. Aurangzeb
		Attorney Docket Number	17403US05
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OTHER ART -- NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
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